



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Motoyuki Tsuihij et al. Art Unit : 2812
Serial No. : 10/781,009 Examiner : Unknown
Filed : February 18, 2004
Title : METHOD OF FABRICATING A SEMICONDUCTOR DEVICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL PRELIMINARY AMENDMENT

Prior to examination, please amend the application as indicated on the following pages.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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